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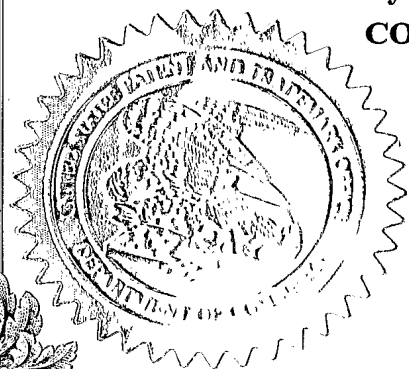
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PROVISIONAL APPLICATION FOR PATENT COVER SHEET

This is a request for filing a PROVISIONAL APPLICATION FOR PATENT under 37 CFR 1.53(c).

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INVENTOR(S)					
Given Name (first and middle [if any])		Family Name or Surname		Residence (City and either State or Foreign Country)	
Charles J.		Razzell		Pleasanton, CA	
Additional inventors are being named on the _____ separately numbered sheets attached hereto					
TITLE OF THE INVENTION (500 characters max)					
Serialization of Multi-Band Sequence Keying For Ultra Wideband...					
Direct all correspondence to: CORRESPONDENCE ADDRESS					
<input checked="" type="checkbox"/> Customer Number: <u>24738</u>					
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ENCLOSED APPLICATION PARTS (check all that apply)					
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<input type="checkbox"/> Application Data Sheet. See 37 CFR 1.76 <u>2 return post cards</u>					
METHOD OF PAYMENT OF FILING FEES FOR THIS PROVISIONAL APPLICATION FOR PATENT					
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.					
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FILING FEE Amount (\$) <u>160-</u>					
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[Page 1 of 2]

Respectfully submitted,

SIGNATURE

Raymond J. Werner

TYPED or PRINTED NAME

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TELEPHONE

503-466-2294Date 02 March 2004

REGISTRATION NO.

34,752

(if appropriate)

Docket Number:

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FEE TRANSMITTAL
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Effective 10/01/2003. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT

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First Named Inventor	Charles A. Razzell
Examiner Name	N/A
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METHOD OF PAYMENT (check all that apply)☒ Check ☐ Credit card ☐ Money Order ☐ Other ☐ None☐ Deposit Account:Deposit Account Number
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☐ Charge fee(s) indicated below ☐ Credit any overpayments☐ Charge any additional fee(s) or any underpayment of fee(s)☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account.**FEE CALCULATION****1. BASIC FILING FEE**

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
1001 770	2001 385	Utility filing fee	
1002 340	2002 170	Design filing fee	
1003 530	2003 265	Plant filing fee	
1004 770	2004 385	Reissue filing fee	
1005 160	2005 80	Provisional filing fee	160-

SUBTOTAL (1) (\$) 160-

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims	Extra Claims	Fee from below	Fee Paid
Independent Claims	-20** =	X	
Multiple Dependent	-3** =	X	

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description
1202 18	2202 9	Claims in excess of 20
1201 86	2201 43	Independent claims in excess of 3
1203 290	2203 145	Multiple dependent claim, if not paid
1204 86	2204 43	** Reissue independent claims over original patent
1205 18	2205 9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$) 0

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)**3. ADDITIONAL FEES**

Large Entity Small Entity

Fee Code (\$)	Fee Code (\$)	Fee Description	Fee Paid
1051 130	2051 65	Surcharge - late filing fee or oath	
1052 50	2052 25	Surcharge - late provisional filing fee or cover sheet	
1053 130	1053 130	Non-English specification	
1812 2,520	1812 2,520	For filing a request for <i>ex parte</i> reexamination	
1804 920*	1804 920*	Requesting publication of SIR prior to Examiner action	
1805 1,840*	1805 1,840*	Requesting publication of SIR after Examiner action	
1251 110	2251 55	Extension for reply within first month	
1252 420	2252 210	Extension for reply within second month	
1253 950	2253 475	Extension for reply within third month	
1254 1,480	2254 740	Extension for reply within fourth month	
1255 2,010	2255 1,005	Extension for reply within fifth month	
1401 330	2401 165	Notice of Appeal	
1402 330	2402 165	Filing a brief in support of an appeal	
1403 290	2403 145	Request for oral hearing	
1451 1,510	1451 1,510	Petition to institute a public use proceeding	
1452 110	2452 55	Petition to revive - unavoidable	
1453 1,330	2453 665	Petition to revive - unintentional	
1501 1,330	2501 665	Utility issue fee (or reissue)	
1502 480	2502 240	Design issue fee	
1503 640	2503 320	Plant issue fee	
1460 130	1460 130	Petitions to the Commissioner	
1807 50	1807 50	Processing fee under 37 CFR 1.17(q)	
1806 180	1806 180	Submission of Information Disclosure Stmt	
8021 40	8021 40	Recording each patent assignment per property (times number of properties)	
1809 770	2809 385	Filing a submission after final rejection (37 CFR 1.129(a))	
1810 770	2810 385	For each additional invention to be examined (37 CFR 1.129(b))	
1801 770	2801 385	Request for Continued Examination (RCE)	
1802 900	1802 900	Request for expedited examination of a design application	

Other fee (specify)

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$) 0

SUBMITTED BY

Name (Print/Type)	Raymond J. Werner	Registration No. (Attorney/Agent)	34,752	Telephone	503-466-2294
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SERIALIZATION OF MULTI-BAND SEQUENCE KEYING FOR ULTRA WIDEBAND RECEIVER ARCHITECTURE WITH REDUCED COMPLEXITY AND POWER CONSUMPTION

5

Inventor: Charles J. Razzell

Field of the Invention

10 The present invention relates generally to wireless communication, and more particularly relates to at least partial serialization of multi-band sequence keying in ultra wideband communications, and to ultra wideband receiver architectures having reduced complexity and reduced power consumption.

Background

15 Ultra wideband (UWB) communications are a form of radio communication in which a signal is spread over a large portion of the radio spectrum, and in which the signal power at any particular frequency is therefore relatively small. The bandwidth of such systems is typically greater than 500MHz, and in some UWB systems the bandwidth may be, for example, between 2GHz and 100GHz. Early UWB systems
20 imparted information to their signals through the use of pulse position modulation. Such pulse position modulation is sometimes referred to in the context of UWB systems as time modulation.

By modulating either the pulse position, that is, the time at which a pulse occurs, or the phase of the pulse, information can be encoded into, and subsequently derived
25 from, a UWB signal. For example, a slightly early pulse may be interpreted as a "zero", while a slightly delayed pulse may be interpreted as a "one". Alternatively, a pulse transmitted at zero phase may be interpreted as a "zero", while a pulse transmitted with 180 degrees may be interpreted as a "one". Unfortunately, these schemes only provide one bit of information per transmitted pulse.

30 One signalling scheme for increasing the information content of UWB signals involves augmenting the phase or pulse position bits with sequence keying. This

technique is sometimes referred to as spectral keying. Sequence keying requires that the UWB waveform consist of independent transmissions in a number of pre-defined frequency bands. In this way, additional information is represented by the time order of the selection of frequency bands for the transmission of UWB pulses. As the number of frequency bands increases, the number of permutations for the time order sequence of frequencies increases rapidly. The use of multiple bands provides some distinct advantages for the implementation of a UWB system, since it allows a reduction in the circuit bandwidths and sample rates by a factor equal to the number of bands employed. Applying sequence keying to the resulting transmitter and receiver is a logical enhancement to increase the information density of such a multi-band UWB system. The apparent disadvantage is that a number of parallel receivers are needed in order to receive the transmitted sequence of tones without prior knowledge of that sequence.

What is needed are methods and apparatus for reducing the complexity and the power consumption of the multi-branch receivers that are required to receive and demodulate the ultra wideband signals transmitted which employ such sequence keying over two or more frequency bands.

Summary of the Invention

Briefly, by partially serializing the transmission of a sequence keyed UWB symbols, the number of parallel receiver paths required to receive such a transmission is reduced. The reduced number of receiver paths are re-used during the reception of each of the partially serialized portions of a UWB symbol, and the local oscillator output frequencies are changed for reception of each of those portions. Reducing the number of parallel receiver paths advantageously reduces the amount of power consumed by such a receiver. Similarly, the amount of area required to implement such a receiver in an integrated circuit is advantageously reduced.

In a further aspect of the present invention, a synchronization sequence uses less than the complete set of frequency bands available for transmission of a UWB symbol.

Brief Description of the Drawings

Fig. 1 is a frequency versus time diagram illustrating one scheme for transmission of symbols in a known ultra wideband communication system.

5 Fig. 2 is a frequency versus time diagram illustrating a partial serialization scheme for transmission of symbols in an ultra wideband communication system in accordance with the present invention.

Fig. 3 is an architectural block diagram of a simplified UWB receiver in accordance with the present invention.

10 Fig. 4 is a flow diagram illustrating an illustrative method in accordance with the present invention.

Detailed Description

15 Generally, the present invention relates to methods and apparatus for reducing the complexity and the power consumption of the multi-branch receivers that are required to receive and demodulate the ultra wideband signals transmitted with two or more frequency bands, i.e., sequence keyed UWB symbols. Similarly, the present invention relates to methods and apparatus for transmitting a partially serialized UWB symbol. Additionally the present invention relates to methods and apparatus for
20 synchronizing a receiver with a transmitter.

A multi-band UWB receiver for which the sequence of arrival times conveys all or part of the modulation information normally requires n parallel receiver branches, or receiver paths, for n bands. In embodiments of the present invention, a partial or full serialization of the transmitted sequence is used to reduce the number of parallel
25 receiver branches by a factor between 2 and n . In other words, the number of portions into which the sequence keyed UWB symbol may be serialized into may be two or greater. This is done without altering the properties of a sequence keying sequence.

Reference herein to "one embodiment", "an embodiment", or similar formulations, means that a particular feature, structure, operation, or characteristic described in connection with the embodiment, is included in at least one embodiment of the present invention. Thus, the appearances of such phrases or formulations herein
5 are not necessarily all referring to the same embodiment. Furthermore, various particular features, structures, operations, or characteristics may be combined in any suitable manner in one or more embodiments.

One modulation scheme for UWB communication is sometimes referred to as spectral keying, which in addition to modulating transmitted pulses in accordance with,
10 for example, BPSK or QPSK modulation techniques, also uses the sequence of transmission of pulses at different frequencies to convey modulation information. For example, five different frequencies may be chosen at a spacing of, for example, 800 MHz. In this example, the pulse duration at each frequency is 3 ns per pulse and
15 therefore 15 ns is required to send the complete sequence. Since, from the perspective of the receiver, the transmitted sequence is unknown ahead of time, parallel receiver paths, or branches, are implemented to allow for any order of arrival.

When considering an implementation of a UWB transceiver in the form of an integrated circuit, one disadvantage of sequence keying is the relatively large analog
20 RF section that results from the need for parallel receiver paths. In particular there are a large number of analog-to-digital (A/D) converters required for implementation of the parallel receiver paths, which, in turn, requires a significant amount of chip area, and, in operation, requires a significant amount of power.

Referring to Fig. 1, a frequency versus time diagram illustrating a sequence keying scheme for transmission of symbols in an ultra wideband communication system is shown. More particularly, Fig. 1 illustrates an example in which a UWB symbol is transmitted, the UWB symbol having eight frequency bands and eight modulation time slots. In this illustrative example, only one carrier (i.e., f1, f2, f3, f4, f5,
25 f6, f7, or f8) can be turned on to transmit during each time slot. Once a carrier has be
30

turned on, it is not turned on again during the transmission of that UWB symbol. As illustrated in Fig. 1, the UWB symbol consists of frequency band f3 at time t1, frequency band f1 at time t2, frequency band f6 at time t3, frequency band f5 at time t4, frequency band f2 at time t5, frequency band f8 at time t6, frequency band f4 at time t7, and frequency band f7 at time t8. This can be expressed in a convenient notation as f3@t1, f1@t2, f6@t3 and so on. A further notational simplification is to express the selected frequencies during the eight slots as {3,1,6,5,2,8,4,7}, where it is understood that the numbers represent which one of the eight carriers is turned on respectively at times t1, t2, t3, ... t8.

In accordance with the present invention, partial serialization of the transmitted UWB signal, results in a reduction in the number of parallel receivers paths required. Consequently, savings in both chip area and power consumption are achieved.

In one scheme for reducing the number of parallel receiver paths in a receiver for a sequence keyed UWB symbol, the sequence is fully serialized by providing individual time slots for each frequency. In general, this requires that n^2 time slots be made available, each of 3 ns in this case. So for $n = 5$, 75 ns is needed between successive symbols. Attempts to increase the pulse repetition rate beyond 13.3 MHz would not be viable without reducing the pulse width. However, reducing the pulse width has several disadvantages, including the fact that the bandwidth occupied by each sub-band would increase, and the number of RAKE fingers needed to collect a substantial portion of the signal energy would also increase.

Embodiments of the present invention represent a compromise between a fully serial and a fully parallel transmission of sequence keying such that the receiver complexity can be cut by a factor of two while still supporting higher pulse repetition factors. Consider, for example, a requirement to use eight sub-carriers with a 3 ns pulse duration and at a symbol repetition rate of 19.5 MHz. For complete serialization, we would need 64 times 3, or 192 ns between each symbol. However, in a UWB receiver in accordance with the present invention, the available time is 51 ns ($1/19.5$ MHz). Consider that we have time for up to two sets of eight pulses ($2 \times 8 \times 3 = 48$ ns). In accordance with the present invention, the number of parallel receiver branches can be

reduced by a factor of 2, while maintaining the original properties of the sequence keying modulation scheme, including information throughput. More particularly, in this case we have 16 phase bits (2 per carrier) and 15 sequence bits since $\log_2(8!) = 15.2$, giving a total of 31 bits per symbol. This gives a raw data rate of $31 \times 19.5 = 604.5$ Mbps.

Referring to Fig. 2, a frequency versus time diagram illustrating a scheme for transmission of UWB symbols in accordance with the present invention is shown. More particularly, Fig. 2 illustrates an example in which the sequence keyed UWB symbol of Fig. 1 has been reformatted in accordance with the invention. As shown in Fig. 2, the UWB symbol consists of a first portion consisting of frequency band f3 at time t11, frequency band f1 at time t12, no frequency band at time t13, no frequency band at time t14, frequency band f2 at time t15, no frequency band at time t16, frequency band f4 at time t17, and no frequency band at time t18; an inter-portion gap (3 ns in this illustrative example); and a second portion of the UWB symbol consisting of no frequency band at time t21, no frequency band at time t22, frequency band f6 at time t23, frequency band f5 at time t24, no frequency band at time t25, frequency band f8 at time t26, no frequency band at time t27, and frequency band f7 at time t28. This can be expressed in a convenient notation as f3@t11, f1@t12, 0@t13, and so on, where "0" represents no transmission in that time slot. A further notational simplification is to express the selected frequencies during the eight time slots of portions 1 and 2, as P1{3,1,0,0,2,0,4,0}, P2{0,0,6,5,0,8,0,7} where it is understood that the numbers represent which one of the eight carriers is turned on respectively at times t11, t12, t13, ... t18 for the first portion (P1) and times t21, t22, t23, ... t28 for the second portion (P2).

With reference to Figs. 1 and 2, it can be seen that all of the temporal ordering of the spectral components shown in the UWB symbol of Fig. 1, can be obtained from the partially serialized UWB symbol of Fig. 2. It will be appreciated by those skilled in the art and having the benefit of this disclosure that the partially serialized sequence keyed UWB symbol of Fig. 2 requires only half the number of parallel receiver branches as compared to a receiver for the sequence keyed UWB symbol shown in Fig. 1.

In one embodiment of the present invention, a receiver, for receiving a UWB symbol having eight spectrally different components, includes four parallel branches. Each complex local oscillator is capable of switching between two available frequencies. During the first half of the symbol interval, frequencies f1, f2, f3, and f4 are provided, whereas during the second half, these are switched over to provide frequencies f5, f6, f7, and f8. Memories are used to store the samples from the first and second halves to allow maximum likelihood decoding of the sequence keying code words. Optional RAKE combining of the significant multipaths is available by making use of additional samples collected by the analog-to-digital converters (ADCs) before switching to the second set of frequencies.

An illustrative embodiment of the present invention is shown in the block diagram of Fig. 3. This embodiment includes a typical radio receiver front end (antenna, filter, and low-noise amplifier), and further shows the output of the low-noise amplifier split and coupled to each of four parallel receiver paths in accordance with the present invention. Further in accordance with the present invention, each of the four parallel receiver paths includes a local oscillator that is operable to produce two different mixing frequencies. In this way, each of the four receiver paths can be used to receive two different frequency bands.

More particularly, a receiver portion 300 of a transceiver includes an antenna 302 that is coupled to a high pass filter 304. High pass filter 304 is coupled to provide a signal to antenna switch 306, antenna switch 306 shown as receiving a control signal from the transmitter portion of the transceiver. Antenna switch 306 is coupled to provide a signal to variable-gain low-noise amplifier (LNA) 308. The output of LNA 308 is coupled to power divider 310. Power divider 308 provides a signal to each of four parallel receiver paths.

A first receiver branch, or path, includes a local oscillator 312. Local oscillator 312 is operable to provide, in response to a control signal from local oscillator control circuit 315, either a first frequency or a second frequency. Local oscillator control circuit 315 can be implemented in a variety of ways, but is basically a timing circuit that recognizes that a synchronization sequence has been received, and thereafter is

operable to provide the control information necessary for the local oscillator in each receiver branch to provide either the first or second frequency at the correct time periods to receive the various portions of the transmitted partially serialized sequence keyed UWB symbol. The output of local oscillator **312** is coupled to a block for producing an in-phase (I) and quadrature (Q) version of the signal. The in-phase signal is coupled to a mixer **316** and the quadrature signal is coupled to a mixer **318**. Each of mixers **316**, **318** receive a signal from power divider **310**. The outputs of mixers **316**, **318** are coupled, respectively, to low pass filters **320**, **322**. The outputs of low pass filters **320**, **322** are coupled, respectively, to variable gain amplifiers **324**, **326**. The outputs of variable gain amplifiers **324**, **326** are coupled, respectively, to A/D converters **328**, **330**. The outputs of A/D converters **328**, **330** are coupled to RAKE combiner **338**. The second, third, and fourth receiver branches are constructed similarly to the arrangement of the first receiver path, with the exception that the local oscillator of each path produces a different pair of frequencies for their respective mixers, and each of those local oscillators is coupled to receive control information from local oscillator control circuit **315**.

Still referring to Fig. 3, the output of RAKE combiner **338** is coupled to a sequence keying demodulator **340**; and the output of sequence keying demodulator **340** is coupled to sequence keying decoder **342**.

Those skilled in the art and having the benefit of this disclosure will understand that such a local oscillator may be designed in a variety of ways that are consistent with the present invention. For example, such a local oscillator may produce one of two frequency outputs responsive to a control signal. This approach has the advantage of using relatively little power, but has the disadvantage of requiring time to switch its frequency output. Alternatively, such a local oscillator may produce two frequency outputs simultaneously and a multiplexer, responsive to a control signal, selects the appropriate frequency output at any given time. This approach has the advantage of requiring very little time to switch between frequencies, but has the disadvantage of consuming additional power to have each frequency output ready concurrently.

Referring to Fig. 4, one embodiment of the present invention is described in

which a receiver is operated so as to synchronize to a UWB signal, and then to receive a first portion and a second portion of partially serialized ultra wideband USB symbol. More particularly, an illustrative method of synchronizing to an ultra wideband signal and receiving a partially serialized, sequence keyed, ultra wideband symbol, includes

5 operating **402** a first plurality of oscillators, each one of the first plurality of oscillators selectively coupled to a respective one of a plurality of parallel receiver paths. As described above the complex local oscillator used in each of the parallel receiver paths may be implemented as, for example, a pair of oscillators and a multiplexer that is used to select between the frequency outputs of each oscillator. Maintaining **404** a second

10 plurality of oscillators in a non-operational low-power state. This helps to reduce power consumption during the period in which the receiver is waiting to determine that a synchronization sequence has been detected. Since the synchronization sequence may be designed with a known sequence of frequency bands that is a subset of the complete set of frequency bands available for transmission of a UWB symbol, only a

15 subset of the local oscillators need to be powered, or operated, at any time while searching for the synchronization sequence. Operating **406** the plurality of parallel receiver paths. In this way the a synchronization sequence may be detected. Determining **408**, based at least in part upon an output of each of the plurality of parallel receiver paths, whether a synchronization sequence has been received. That

20 is, if the predetermined sequence has been received then the receiver is synchronized to the transmitter, and the receiver is not ready to receive the partially serialized sequence keyed UWB symbol. Receiving **410** a first portion of a partially serialized, sequence keyed, ultra wideband symbol. Operating **412**, if the synchronization sequence has been detected, a second plurality of oscillators, and selectively coupling

25 **414** each one of the second plurality of oscillators to a respective one of the plurality of parallel receiver paths. In this way, the local oscillators will be able to provide the mixers in each of the receiver paths with new frequencies that enable the reception of a subsequent portion of the partially serialized sequence keyed ultra wideband symbol. Receiving **416** a second portion of the partially serialized, sequence keyed, ultra

30 wideband symbol.

In typical embodiments of the present invention, the frequency carriers are

turned on at the beginning of a modulation time slot and turned off at the end of that modulation time slot. However, in alternative embodiments of the present invention, identical temporal alignment of frequency carrier operation and modulation time slot boundaries may not be required, but rather some accommodation is provided for recognizing a symbol wherein there is some misalignment between the time of
5 frequency carrier operation and the modulation time slot boundaries.

Conclusion

Various embodiments of the present invention include methods and apparatus
10 for achieving enhanced sequence keying by reducing the number of parallel receiver branches by a factor at least two, while maintaining the original properties of the sequence keying modulation. The present invention utilizes a compromise between a fully parallel and a fully serial approach. In other words, a first set of bits is transmitted in using a first set of carriers, and this first transmission is then serially followed by
15 transmission of a second set of bits using a second set of carriers.

In various alternative embodiments, a receiver for receiving partially serialized, sequence keyed UWB symbols in accordance with the present invention, may be combined with a transmitter so as to form a transceiver. In such arrangements, an antenna may be shared between the transmitter and the receiver. In some transceiver
20 embodiments, the transmitter and receiver circuits may be incorporated in a single integrated circuit.

An advantage of some embodiments of the present invention, is that since only one set of carriers is active at a time, the complexity of the receiver can be significantly reduced.

25 An advantage of some embodiments of the present invention, is that by reducing the number of parallel receiver branches required, the area of a chip in which such a receiver is implemented is also reduced.

An advantage of some embodiments of the present invention, is that by reducing the amount of circuitry required to implement a receiver, the amount of power consumption is reduced.

5 It is to be understood that the present invention is not limited to the embodiments described above, but encompasses any and all embodiments within the scope of the subjoined Claims.

What is Claimed is:

1. A method of synchronizing to an ultra wideband signal and receiving a partially serialized, sequence keyed, ultra wideband symbol, comprising:

a) operating a first plurality of oscillators, each one of the first plurality of oscillators selectively coupled to a respective one of a plurality of parallel receiver paths;

b) maintaining a second plurality of oscillators in a non-operational low-power state;

b) operating the plurality of parallel receiver paths;

c) determining, based at least in part upon an output of each of the plurality of parallel receiver paths, whether a synchronization sequence has been received;

d) receiving a first portion of a partially serialized, sequence keyed, ultra wideband symbol;

e) operating, if the determination of (c) is affirmative, a second plurality of oscillators;

f) selectively coupling each one of the second plurality of oscillators to a respective one of the plurality of parallel receiver paths; and

g) receiving a second portion of the partially serialized, sequence keyed, ultra wideband symbol.

2. The method of Claim 1, wherein each of the plurality of receiver paths receives an in-phase and quadrature version of the outputs of the oscillators coupled thereto.

3. The method of Claim 2, wherein the non-operational low-power state comprises a de-energized state.

4. The method of Claim 2, wherein the non-operational low-power state comprises an energized, non-switching state.

5. The method of Claim 2, further comprising decoupling the first plurality of oscillators from the plurality of parallel receiver paths subsequent to receiving the first portion of a partially serialized, sequence keyed, ultra wideband symbol.

6. A method of receiving a partially serialized, sequence keyed, ultra wideband symbol, comprising:

a) operating a plurality of local oscillators, each local oscillator operable to selectively produce at least two frequency outputs, and further configured to produce an in-phase and a quadrature version of each of the at least two frequency outputs;

b) operating the plurality of parallel receiver paths, each one of the plurality of parallel receiver paths coupled to a respective one of the local oscillators and receiving a first in-phase and quadrature pair of signals at a first one of the at least two frequencies;

c) receiving a first portion of a partially serialized, sequence keyed, ultra wideband symbol;

d) operating the plurality of local oscillators such that each one of the plurality of

parallel receiver paths receives, from its corresponding local oscillator, a second in-phase and quadrature pair of signals at a second one of the at least two frequencies; and

g) receiving a second portion of the partially serialized, sequence keyed, ultra wideband symbol.

7. The method of Claim 6, wherein there is time delay between the end of the first portions of the partially serialized, sequence keyed, ultra wideband symbol and the second portion of the partially serialized, sequence keyed, ultra wideband symbol.

8. The method of Claim 7, further comprising synchronizing to an ultra wideband signal prior to receiving the first portion of the partially serialized, sequence keyed, ultra wideband symbol.

9. The method of Claim 8, wherein the local oscillators are operable to selectively produce at least two frequency outputs subsequent to synchronizing to an ultra wideband signal.

10. The method of Claim 9, wherein the local oscillators are operable to selectively produce only one frequency output prior to synchronizing to an ultra wideband signal.

11. A receiver for receiving a partially serialized, sequence keyed, ultra wideband symbol, comprising:

a plurality of parallel receiver paths, each receiver path including a first mixer and a second mixer,

a plurality of complex local oscillators, each complex local oscillator having an in-phase output and a quadrature output, each one of the complex local oscillators coupled to a corresponding one of the plurality of receiver paths such that the in-phase output is coupled to the corresponding first mixer and the quadrature output is coupled to the corresponding second mixer;

a rake combiner coupled to receive an output from each of the plurality of parallel receiver paths;

a sequence keying demodulator coupled to the rake combiner; and

a sequence keying decoder coupled to the sequence keying demodulator.

12. The receiver of Claim 11, wherein each receiver path further comprises a pair of analog-to-digital converters.

13. The receiver of Claim 12, further comprising a memory coupled to store information received from the first portion of the partially serialized, sequence keyed, ultra wideband symbol.

14. The receiver of Claim 13, further comprising control circuitry for determining whether a synchronization sequence has been received and for providing control signals that determine the frequency output of each of the plurality of complex local oscillators.

15. The receiver of Claim 14, further comprising an antenna, a filter coupled to the antenna, an antenna switch coupled to the filter, a variable-gain low-noise amplifier coupled to the antenna switch, and a power divider coupled to the variable-gain low-noise amplifier, the power divider further coupled to each of the plurality of parallel receiver paths.

16. A transceiver, comprising:

a receiver for receiving a partially serialized, sequence keyed, ultra wideband symbol, comprising:

a plurality of parallel receiver paths, each receiver path including a first mixer and a second mixer,

a plurality of complex local oscillators, each complex local oscillator having an in-phase output and a quadrature output, each one of the complex local oscillators coupled to a corresponding one of the plurality of receiver paths such that the in-phase output is coupled to the corresponding first mixer and the quadrature output is coupled to the corresponding second mixer;

a rake combiner coupled to receive an output from each of the plurality of parallel receiver paths;

a sequence keying demodulator coupled to the rake combiner;

a sequence keying decoder coupled to the sequence keying demodulator;

a memory coupled to store information received from the first portion of the partially serialized, sequence keyed, ultra wideband symbol;

control circuitry for determining whether a synchronization sequence has been received and for providing control signals that determine the frequency output of each of the plurality of complex local oscillators; and

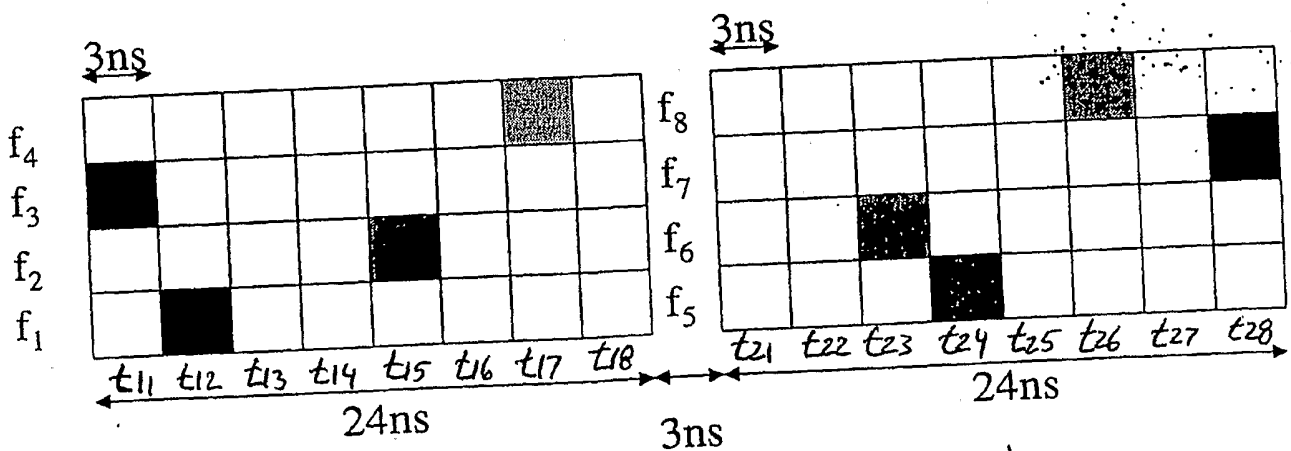
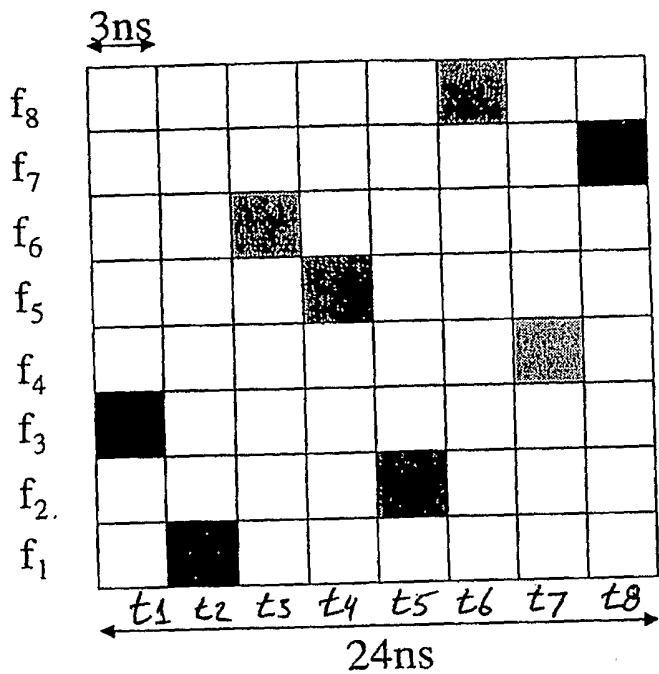
an antenna, a filter coupled to the antenna, an antenna switch coupled to the filter, a variable-gain low-noise amplifier coupled to the antenna switch, and a power divider coupled to the variable-gain low-noise amplifier, the power divider further coupled to each of the plurality of parallel receiver paths; and

a transmitter coupled to the antenna switch.

SERIALIZATION OF MULTI-BAND SEQUENCE KEYING FOR ULTRA WIDEBAND RECEIVER ARCHITECTURE WITH REDUCED COMPLEXITY AND POWER CONSUMPTION

ABSTRACT OF THE DISCLOSURE

By partially serializing the transmission of a sequence keyed UWB symbol, the number of parallel receiver branches required to receive such a transmission is reduced. The reduced number of receiver paths are re-used during the reception of each of the partially serialized portions of a UWB symbol, and the local oscillator output frequencies are changed for reception of each of those portions. Reducing the number of parallel receiver paths advantageously reduces the amount of power consumed by such a receiver. Similarly, the amount of area required to implement such a receiver in an integrated circuit is advantageously reduced. In a further aspect of the present invention, a synchronization sequence uses less than the complete set of frequency bands available for transmission of a UWB symbol.



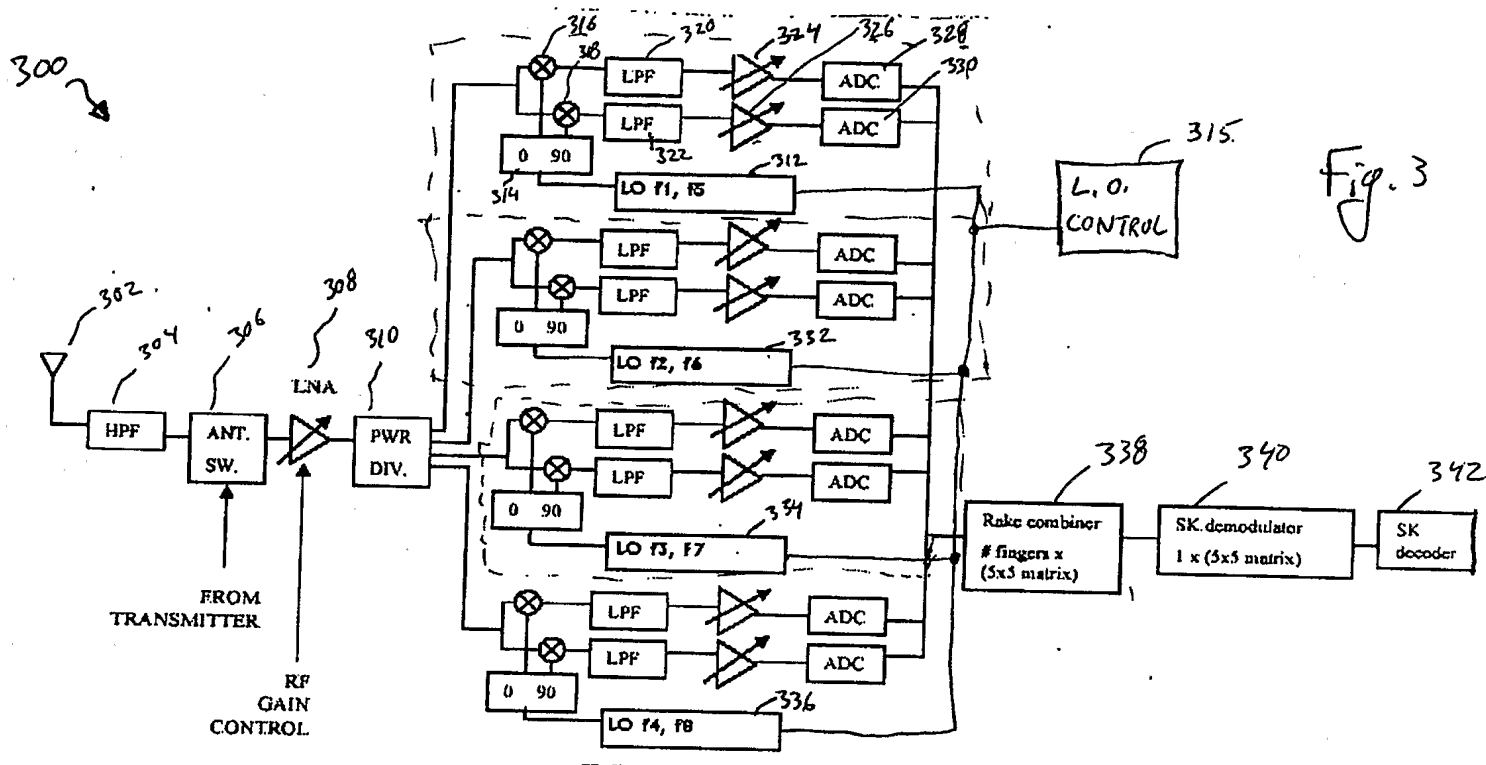


Fig. 3

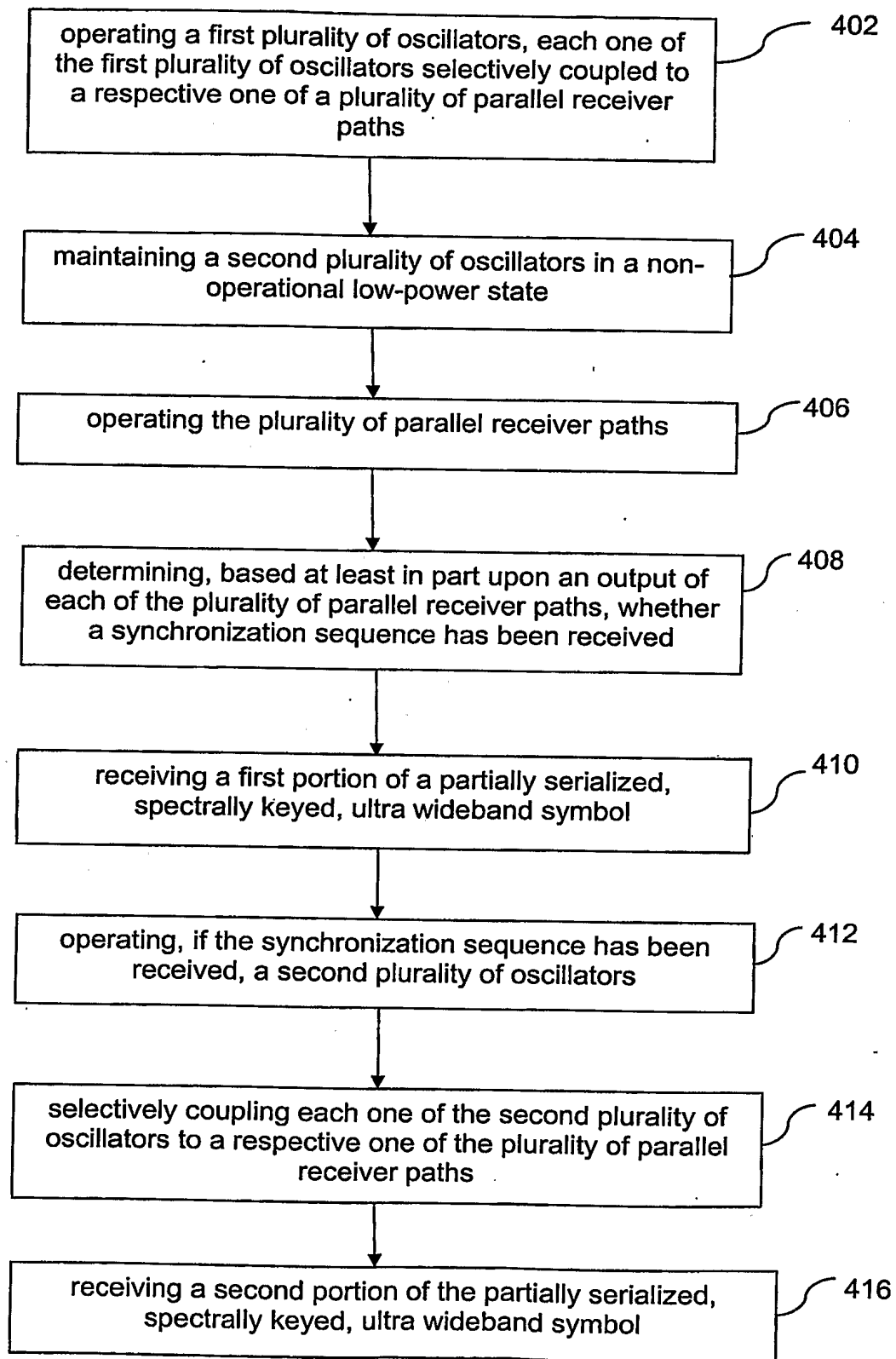


FIG. 4